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Patent 🖾	Trac	temark 🚨	520.	38856X00
Serial No		Filed _	August 8	, 2000
Applicant(s) _	NOBORU MA	SUDA, et	al	·
Papers filed he	erewith on	·	August	8, 2000
KK Fees \$_	730.00	XX Assignme	nt w/3669	U.S. PTO
KKNew Application w/decl 🗅 Letter to Draftsman 09/634544				
□ Amendment		☐ Priority Documents		
□ Notice of Appeal		Petition for Ext. of Time/08/00		
☐ Appeal Brief		KK 10 Sh	eets of Formal	Drawings
KKOther Specification, (10) Claims, ABstract (19pgs.				
in Japanese language), Declaration (4pgs.)				
Figs. 1-14, Credit Card Payment Form, List				
Receipt is hereb identified case.	Copies of Pri	or Art Repapers filed as in SSIONER OF P	dicated in connect	tion with above RADEMARKS

[VOLTAGE CONTROLLED OSCILLATOR AND PLL CIRCUIT USING THE SAME]
(N. MASUDA et al.)

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## LIST OF THE PRIOR ART REFERENCES CITED IN THE SPECIFICATION

- 1. 1998 IEEE International Solid-State Circuits Conference,
  DIGEST OF TECHNICAL PAPERS, pp.396-397, (1998).
- 2. JP 11-15541 A
- 3. JP 09-074352 A

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